L Number	Hits	Search Text	DB	Time stamp
1	4414	((memory storage) near5 (device module block sector page)) same (cach$3 storage buffer register directory queue) same ((dirty evict$3 defect$3 default$3) near4 (buffer register directory cach$3 storage queue memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 17:31
2	1517	((memory storage) near5 (device module block sector page)) same (cach$3 storage buffer register directory queue) same ((dirty evict$3 defect$3 default$3) near4 (buffer register directory cach$3 storage queue memory)) same ((one single unit) near5 (memory device storage chip circuit logic))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 17:32
3	1543	((one single unit "SAME") near4 (memory storage device chip circuit logic)) same (((memory storage) near5 (device module block sector page)) same (cach$3 storage buffer register directory queue) same ((dirty evict$3 defect$3 default$3) near4 (buffer register directory cach$3 storage queue memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 17:33
5	123	((every per one single) with ((evict$4 dirty) near4 (buffer register cach$3 memory storage directory table))) same (((one single unit "SAME") near4 (memory storage device chip circuit logic)) same (((memory storage) near5 (device module block sector page)) same (cach$3 storage buffer register directory queue) same ((dirty evict$3 defect$3 default$3) near4 (buffer register directory cach$3 storage queue memory))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 17:51
6	84	711/$.ccls. and (((every per one single) with ((evict$4 dirty) near4 (buffer register cach$3 memory storage directory table))) same (((one single unit "SAME") near4 (memory storage device chip circuit logic)) same (((memory storage) near5 (device module block sector page)) same (cach$3 storage buffer register directory queue) same ((dirty evict$3 defect$3 default$3) near4 (buffer register directory cach$3 storage queue memory)))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 17:35
7	30	((every per one single) with ((evict$4 dirty) near4 (buffer register cach$3 memory storage directory table))) same (((within in locat$3 includ$3 compris$3) near4 (one single "SAME")) with (memory storage device module block sector page)) same (((one single unit "SAME") near4 (memory storage device chip circuit logic)) same (((memory storage) near5 (device module block sector page)) same (cach$3 storage buffer register directory queue) same ((dirty evict$3 defect$3 default$3) near4 (buffer register directory cach$3 storage queue memory))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 17:54

L Number	Hits	Search Text	DB	Time stamp
1	2	("5900011").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 18:44
2	9	5900011.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 18:46
3	198	(((single one off) near4 (memory storage device module)) with (within locat$3 in)) same (cach$3 memory storage buffer) with ((victim evict$3 dirty) near4 (memory buffer register cach$3 storage directory table list queue))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 18:49
5	9	((command request$3) near4 (sequenc$3 serializ$6)) and ((((stor$3 writ$3 download$3) near3 back) same ((victim dirty evict$4) near5 (line data))) and ((((single one off) near4 (memory storage device module)) with (within locat$3 in)) same (cach$3 memory storage buffer) with ((victim evict$3 dirty) near4 (memory buffer register cach$3 storage directory table list queue))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 18:51
6	17	((command request$3) near4 (sequencer serializ$6)) and ((((single one off) near4 (memory storage device module)) with (within locat$3 in)) same (cach$3 memory storage buffer) with ((victim evict$3 dirty) near4 (memory buffer register cach$3 storage directory table list queue)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 18:52
7	3	((command request$3) near4 (sequencer serializ$6)) and ((((stor$3 writ$3 download$3) near3 back) same ((victim dirty evict$4) near5 (line data))) and ((((single one off) near4 (memory storage device module)) with (within locat$3 in)) same (cach$3 memory storage buffer) with ((victim evict$3 dirty) near4 (memory buffer register cach$3 storage directory table list queue))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 18:52
4	80	(((stor$3 writ$3 download$3) near3 back) same ((victim dirty evict$4) near5 (line data))) and ((((single one off) near4 (memory storage device module)) with (within locat$3 in)) same (cach$3 memory storage buffer) with ((victim evict$3 dirty) near4 (memory buffer register cach$3 storage directory table list queue)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/03 18:53